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U.S. UTILITY Patent Application

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**APPLICATION NO.** 09/882722 CONT/PRIOR CLASS SUBCLASS ART UNIT EXAMINER 2841 2827 260 APPLICANTS Robert Clarkson Glen Hardins

Printed circuit board having a microelectronic semiconductor device mount area for trace routing therethrough

PTO-2040 12/99

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
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